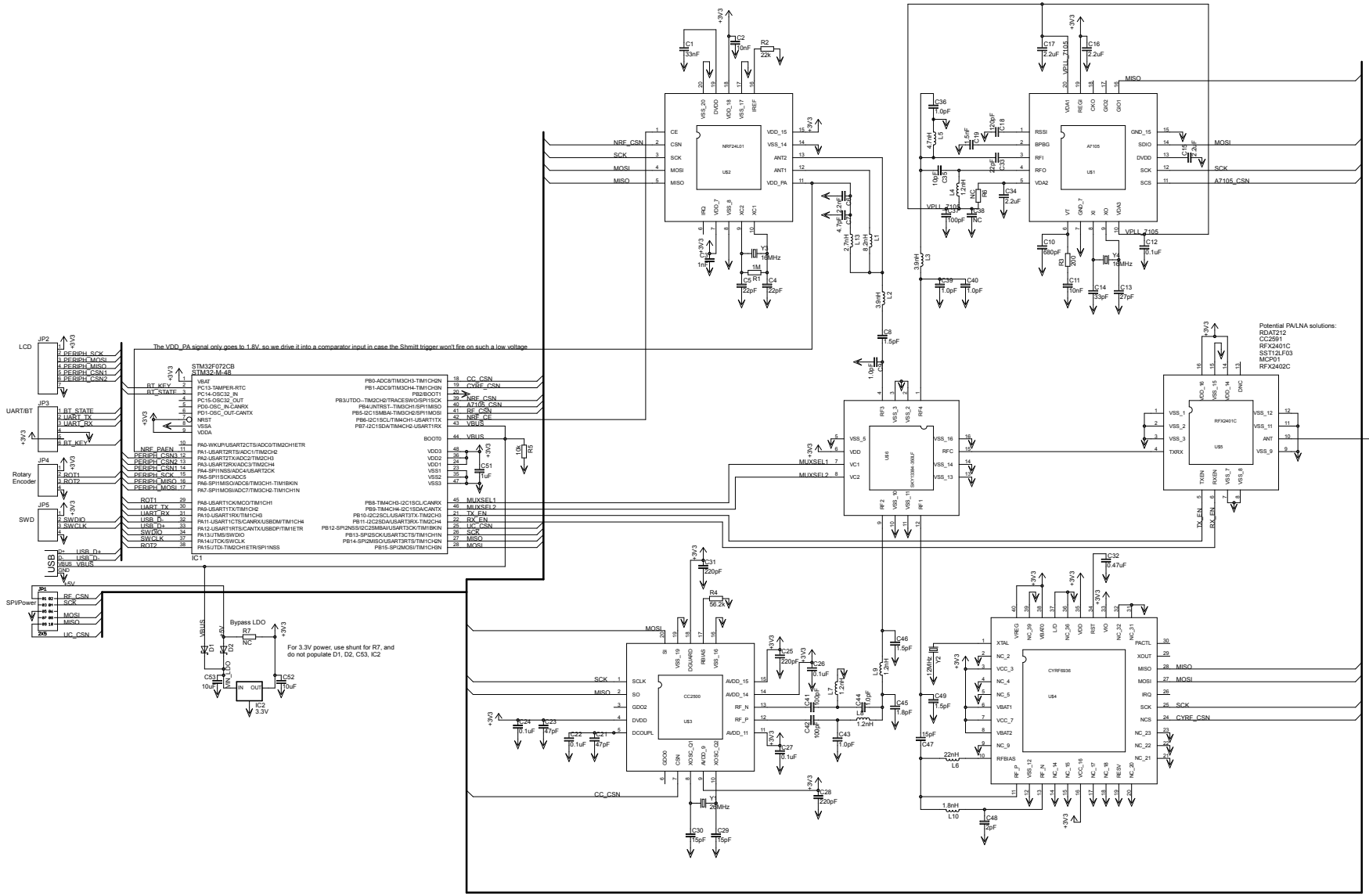
The VDD_PA signal only goes to 1.8V, so we drive it into a comparator input in case the Shmitt trigger won't fire on such a low voltage
STM32F072CB
STM32-M-48
IC1
NRF24L01
U52
A7105
U51
CC2500
U53
CYRF6936
U54
SKY13384-359LF
U56
RFX2401C
U55
Potential PA/LNA solutions:
RDAT212
CC2591
RFX2401C
SST12LF03
MCP01
RFX2402C
Bypass LDO
For 3.3V power, use shunt for R7, and do not populate D1, D2, C53, IC2
IC2
3.3V
LCD
UART/BT
Rotary Encoder
SWD
USB
SPI/Power
J1