



Fig 4.1 A7105 QFN Package Top View

Pin No.	Symbol	I/O	Function Description
1	RSSI	AO	External pin connected to bypass capacitor for RSSI reading or input pin for ADC.
2	BPBG	AO	Regulator bias point
3	RFI	AI	Low noise amplifier input.
4	RFO	AO	Power amplifier output.
5	VDA2	I	Voltage supply for RX & TX analog part
6	VT	AI	VCO frequency control input, internal connected to PLL charge pump
7	GND	I	Ground
8	XI	AI	Crystal oscillator input node
9	XO	AO	Crystal oscillator output node
10	VDA3	I	Voltage supply for PLL part
11	SCS	DI	3 wire SPI chip select
12	SCK	DI	3 wire SPI clock input pin
13	DVDD	I	Connected to a bypass capacitor to supply voltage for digital part
14	SDIO	DI/O	3 wire SPI read/write data pin
15	GND	I	Ground
16	GIO1	DI/O	Multi-function GIO1 / 4-wire SPI data output
17	GIO2	DI/O	Multi-function GIO2 / 4-wire SPI data output
18	CKO	DO	Multi-function clock output
19	REGI	AI	Regulator input
20	VDA1	PO	Regulator output and voltage supply of IF part

Note : A : (Analog)    D : (Digital)    I : (Input)    O : (Output)    P : (Power)